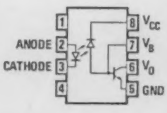
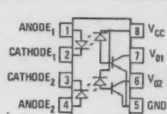
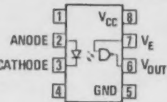
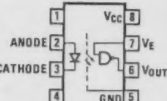
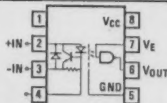
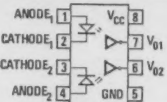
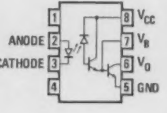
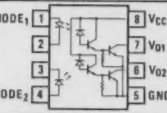
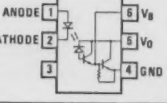


Device	Description	Application <sup>[1]</sup>	Typical Data Rate (NRZ)	Current Transfer Ratio	Specified Input Current	Input To Output Insulation	Page No.
	6N135 (5082-4350)	Transistor Output Line Receiver, Analog Circuits, TTL/CMOS, TTL/LSTTL Ground Isolation	1M bit/s	7% Min.	16mA	3000Vdc <sup>[3]</sup>	186
	6N136 (5082-4351)			19% Min.			
	HCPL-2502 (5082-4352)			15-22% <sup>[2]</sup>			
	HCPL-2530 (5082-4354)	Dual Channel Transistor Output Line Receiver, Analog Circuits, TTL/CMOS, TTL/LSTTL Ground Isolation	1M bit/s	7% Min.	16mA	3000Vdc <sup>[3]</sup>	190
	HCPL-2531 (5082-4355)			19% Min.			
	6N137 (5082-4360)	Optically Coupled Logic Gate Line Receiver, High Speed Logic Ground Isolation	10M Bit/s	700% Typ.	5.0mA	3000Vdc <sup>[3]</sup>	194
	HCPL-2601 (5082-4361)	High Common Mode Rejection, Optically Coupled Logic Gate Line Receiver, High Speed Logic Ground Isolation In High Ground or Induced Noise Environments	10M bit/s	700% Typ.	5.0mA	3000Vdc <sup>[3]</sup>	198
	HCPL-2602	Optically Coupled Line Receiver Replace Conventional Line Receivers In High Ground or Induced Noise Environments	10M bit/s	700% Typ.	5.0mA	3000Vdc <sup>[3]</sup>	202
	HCPL-2630 (5082-4364)	Dual Channel Optically Coupled Gate Line Receiver, High Speed Logic Ground Isolation	10M bit/s	700% Typ.	5.0mA	3000Vdc <sup>[3]</sup>	208

## Low Input Current/High Gain Optocouplers

Device	Description	Application <sup>[1]</sup>	Typical Data Rate (NRZ)	Current Transfer Ratio	Specified Input Current	Input To Output Insulation	Page No.
	6N138 (5082-4370)	Low Saturation Voltage, High Gain Output, $V_{CC}=7V$ Max.	300k bit/s	300% Min.	1.6mA	3000Vdc <sup>[3]</sup>	212
	6N139 (5082-4371)	Low Saturation Voltage, High Gain Output, $V_{CC}=18V$ Max.		400% Min.	0.5mA		
	HCPL-2730	Dual Channel, High Gain, $V_{CC}=7V$ Max.	300k bit/s	300% Min.	1.6mA	3000Vdc <sup>[3]</sup>	216
	HCPL-2731	Dual Channel, High Gain, $V_{CC}=18V$ Max.		400% Min.	0.5mA		
	4N45	Darlington Output $V_{CC}=7V$ Max.	3k bit/s	250% Min.	1.0mA	3000Vdc <sup>[3]</sup>	220
	4N46	Darlington Output $V_{CC}=20V$ Max.		350% Min.	0.5mA		